

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 09-292596

(43)Date of publication of application : 11.11.1997

(51)Int.Cl.

G02F 1/133

G09G 3/18

(21)Application number : 08-105242

(71)Applicant : NEC CORP

(22)Date of filing : 25.04.1996

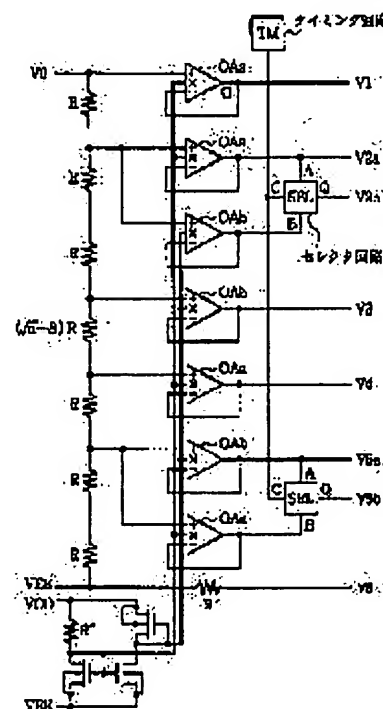
(72)Inventor : SEKO YOSHIKAZU

(54) LIQUID CRYSTAL DRIVING POWER SOURCE CIRCUIT

(57)Abstract:

PROBLEM TO BE SOLVED: To prevent distortion in a waveform and deterioration in picture quality by voltage dividing between plural source potential and switching an operational amplifier circuit receiving its output voltage and charge driving a liquid crystal display element with the operational amplifier circuit receiving the output voltage and discharge driving the liquid crystal display element at the prescribed timing.

SOLUTION: Resistors R potential divide between a plus side power source V0 and a minus side power source VEE, and operational amplifiers OAa, OAb impedance convert the divided voltage to supply liquid crystal element driving power sources (V1, V2a, V2b, V3, V4, Va, V5b). A selector circuit SEL controlled by a timing generation circuit TM switches the operational amplifiers OAa, OAb at the switch timing considering directions of charge/discharge for liquid crystal load capacity related to the voltages V2b, V5b to output the voltages V2b, V5b. Thus, the distortion in the waveform and the deterioration in the picture quality due to the drive power shortage of the power source are prevented even in a display device, etc., with large capacity of liquid crystal material.



LEGAL STATUS

[Date of request for examination]

25.04.1996

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the
examiner's decision of rejection or application
converted registration]

[Date of final disposal for application]

[Patent number] 2865053

[Date of registration] 18.12.1998

[Number of appeal against examiner's decision of
rejection]

[Date of requesting appeal against examiner's
decision of rejection]

[Date of extinction of right]

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(19) 日本国特許庁 (J P)

(12) 公開特許公報 (A)

(11) 特許出願公開番号

特開平9-292596

(43) 公開日 平成9年(1997)11月11日

(51) Int.Cl. ⁸	識別記号	庁内整理番号	F I	技術表示箇所
G 0 2 F 1/133	5 2 0		G 0 2 F 1/133	5 2 0
G 0 9 G 3/18			G 0 9 G 3/18	

審査請求 有 請求項の数3 O L (全 5 頁)

(21) 出願番号 特願平8-105242

(22) 出願日 平成8年(1996)4月25日

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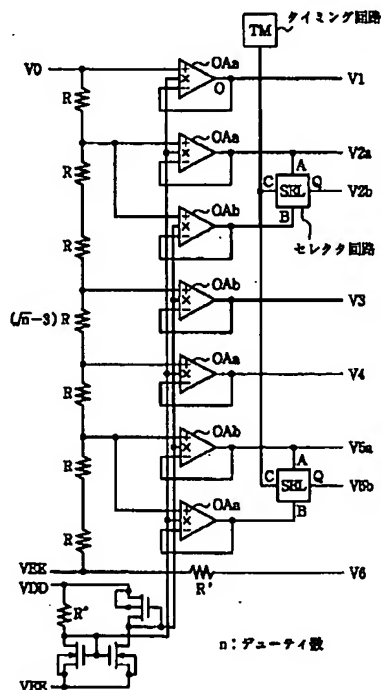
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(54) 【発明の名称】 液晶駆動用電源回路

(57) 【要約】

【課題】低消費電力でかつ波形の歪や液晶画質の悪化を防止するための液晶駆動用電源電圧を供給することを目的とする。

【解決手段】液晶表示素子を駆動するためのオペアンプ回路を充電用・放電用の2回路を別々に有し、上記回路を充放電のタイミングにより切り換えるためのスイッチ回路およびそのタイミングを発生させるためのタイミング回路を有する。これにより各オペアンプ回路の低消費電力化をはかる。



【特許請求の範囲】

【請求項1】 第1の電源電位と第2の電源電位との間を分圧する複数の抵抗を有する抵抗分圧回路と、この抵抗分圧回路の出力電圧を入力に受け液晶表示素子を充電駆動する第1のオペアンプ回路と、前記抵抗分圧回路の出力電圧を入力に受け前記液晶表示素子を放電駆動する第2のオペアンプ回路と、前記第1のオペアンプ回路と前記第2のオペアンプ回路を所定のタイミングで切り換える第1のスイッチ回路と、この第1のスイッチ回路を制御するタイミング回路とを有することを特徴とする液晶駆動用電源回路。

【請求項2】 前記抵抗分圧回路の出力電圧を受け前記タイミング回路により前記抵抗分圧回路の供給レベル電圧を切り換える第2のスイッチ回路を有することを特徴とする請求項1記載の液晶駆動用電源回路。

【請求項3】 前記第1のスイッチ回路と前記第2のスイッチ回路とが前記タイミング回路により同期して動作する請求項2記載の液晶駆動用電源回路

【発明の詳細な説明】

【0001】

【発明の属する技術分野】本発明は液晶表示装置に係り、特に時分割数（デューティ）が大きい中型又は大型のドットマトリックスタイプの液晶表示素子に最適な液晶駆動用電源回路に関する。

【0002】

【従来の技術】図6を参照すると、従来の液晶駆動回路は、この液晶駆動回路における液晶電圧をプラス側電源（例えば+10V）とマイナス側電源（例えば0V）との電圧差を内部抵抗により分圧し、さらにボルテージフォロア型オペアンプによりインピーダンス変換したものを直接液晶駆動電圧とする。

【0003】このような従来の液晶駆動回路は、例えば特開平5-273932号公報に開示されている。この従来の回路構成では中型又は大型の液晶表示素子を駆動する場合、各オペアンプの駆動能力を上げて用いる。

【0004】

【発明が解決しようとする課題】しかしながら従来の回路構成では以下に示す問題点がある。

【0005】（1）液晶の表示素子数が大きくなるとそれだけ電源の駆動能力が必要になるため、各オペアンプの駆動能力を上げる必要があり、消費電流が大きくなってしまう。

【0006】（2）液晶素子は一般にはコモン電極とセグメント電極間に電圧を印加することにより表示することを可能にするが、セグメント数つまり表示行数が多くなれば、コモン電極は共通であるため、各コモンにかかる負荷容量（液晶素子は容量負荷であるため）が大きくなるが図6に示す様に例えばデューティ数がnの場合、n番目のコモンに関しては液晶負荷からの充放電が1番目から（n-1）番目までのコモンに比べ放電電荷が大

きく、その結果n番目コモンの駆動能力を考えると充電放電の両方の能力を持ったオペアンプを使用する必要があり消費電力を極力小さくすることができないという問題がある。

【0007】

【課題を解決するための手段】本発明の液晶駆動用電源回路は、第1の電源電位と第2の電源電位との間を分圧する複数の抵抗を有する抵抗分圧回路と、この抵抗分圧回路の出力電圧を入力に受け液晶表示素子を充電駆動する第1のオペアンプ回路と、前記抵抗分圧回路の出力電圧を入力に受け前記液晶表示素子を放電駆動する第2のオペアンプ回路と、前記第1のオペアンプ回路と前記第2のオペアンプ回路を所定のタイミングで切り換える第1のスイッチ回路と、この第1のスイッチ回路を制御するタイミング回路とを有する構成である。

【0008】また、前記抵抗分圧回路の出力電圧を受け前記タイミング回路により前記抵抗分圧回路の供給レベル電圧を切り換える第2のスイッチ回路を有する構成とすることもできる。

【0009】さらにまた、本発明の液晶駆動用電源回路の前記第1のスイッチ回路と前記第2のスイッチ回路とが前記タイミング回路により同期して動作する構成とすることもできる。

【0010】

【発明の実施の形態】本発明の第1の実施の形態を図を参照して説明する。

【0011】図1を参照すると本実施の形態の液晶駆動用電源回路は、プラス側電源V0とマイナス側電源VEEの両電源間を電位分割する抵抗Rと、それぞれ分割した電圧をインピーダンス変換し、液晶素子駆動用電源（V1, V2a, V2b, V3, V4, Va, V5b）の7レベルを供給するオペアンプ（OAa, OAb）を備える。さらに、電圧V2bおよびV5bについては液晶負荷容量に対する充放電の向きを考慮した図2に示す様な切換えタイミングにより（A, Bに示すコモンn番目の波形エッジ）アンプOAa, OAbを切換えて電圧V2b, V5bをそれぞれ出力するセクタ回路SELおよびセクタ回路SELを制御するタイミング発生回路TMを備える。各基準電圧（V1～V6）とコモンとの対応は、デューティ数nの液晶装置を例にすると、コモン1番目～（n-1）番目は電圧V1, V2a, V5a, V6の各レベルを選択し、コモンのn番目は電圧V1, V2b, V5b, V6レベルを選択することになる。

【0012】次に、回路上の抵抗Rの設定について説明すると、まず抵抗Rは基本的に電源間V0～VEEを単に分圧するだけであり、電流値を小さくするために大きな定数のものを選ぶ。また抵抗R'はオペアンプ（OAa, 又はOAb）のインピーダンスとほぼ等しい定数を選び全体のバランスを均等にする。

【0013】抵抗 R'' は各オペアンプでの低電流回路に流す電流値を決める抵抗であり、全体の消費電流を小さくするために数百 $K\Omega$ 程度の大きなものを選択する。

【0014】また図2に示すパルス幅 t_s 及び t_h についてはセグメント、コモン出力の応答時間を考慮して、数十 μs 程度で設定することによりタイミングのずれによる表示への影響がない様に十分時間をとる。

【0015】次に、本発明の第2の実施の形態を図4および図5を参照して説明する。

【0016】図4を参照すると、この実施の形態は、第1の電源電位 V_0 をレベル電圧 V_1 に調整する抵抗 R' と抵抗分圧回路の出力電圧のうち2個の出力電圧を切り換えるセクタ回路 SEL_2 をさらに備える以外は第1の実施の形態と同一構成であり、同一の構成要素には同一参照符号を付して図示してある。

【0017】この実施の形態のタイミング回路 TM は図5に示すようにセクタ回路 SEL_1 を駆動する出力 O_1 とセクタ回路 SEL_2 を駆動する出力 O_2 を同期して出力しこの実施の形態の液晶表示駆動電圧($V_1 \sim V_6$)はそれぞれセクタ回路 SEL_1 、 SEL_2 の切り換えにより図5に示す波形を出力する。

【0018】本発明の第2の実施の形態は、電圧 V_0 を調整する場合の回路構成であり、プラス側電源電位 V_0 を高圧系電源 VDD として、内部又は外部より供給する場合、オペアンプ回路 OA_a を用いてインピーダンス変換する必要がない。したがって出力レベル V_1 のオペアンプ回路 OA_a は不要となる。また出力レベル V_2a を入力するオペアンプ OA_a とレベル V_4 を入力するオペアンプの OA_a とは同一時間内では同時に選択されないため共用することが可能であり、出力レベル V_5 を入力するオペアンプ OA_b と出力レベルを入力するオペアンプ OA_b もまた共用可能となる。従ってオペアンプ回路を第1の実施の形態より3個削除して同一の効果を得る

ことが可能となり、さらに低消費電力化をはかることができる。

【0019】

【発明の効果】以上説明した様に本発明によれば、液晶材の容量が大きい表示装置や、大型パネルの液晶表示素子においても、電源の駆動能力不足による波形の歪や画質の悪化を防止することができかつ電源回路を有する液晶駆動用の半導体素子の消費電力を小さくする効果がある。

【図面の簡単な説明】

【図1】本発明の液晶駆動用電源回路の第1の実施の形態の回路図である。

【図2】図1に示したタイミング回路の出力タイミング波形である。

【図3】図1に示したオペアンプ回路の回路図である。

【図4】本発明の第2の実施の形態の回路図である。

【図5】図4に示したタイミング回路の出力波形である。

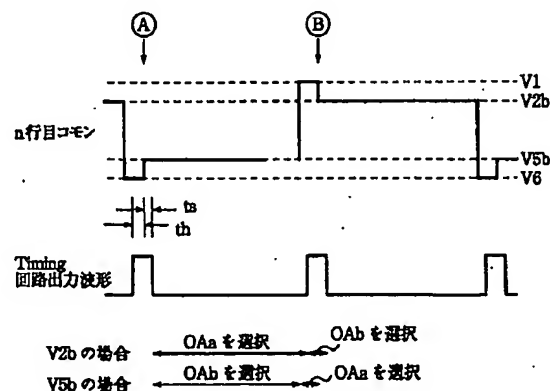
【図6】従来の液晶駆動用電源回路の回路図である。

【図7】図6の電圧 $V_1 \sim V_6$ と液晶駆動時のコモンおよびセグメント波形を示すタイミングチャートである。

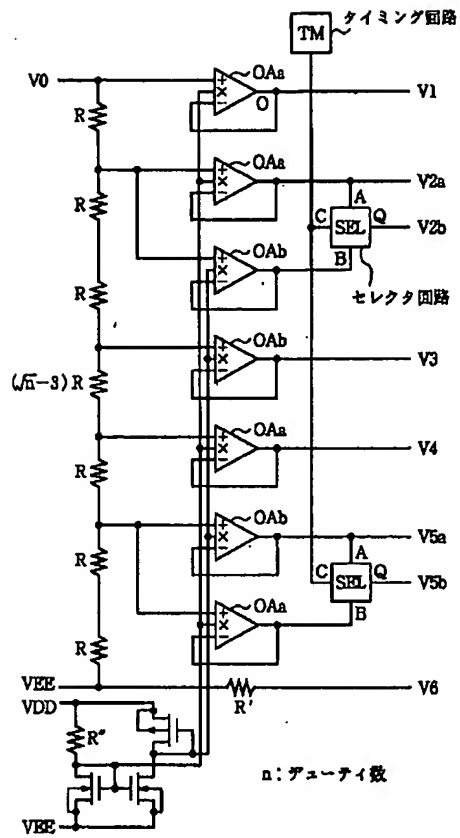
【符号の説明】

$V_1 \sim V_6$ 液晶駆動電圧
 V_0 プラス電源
 V_{EE} マイナス電源
 OA_a, b オペアンプ
 R ブリッジ抵抗
 R' 補助抵抗
 R'' バイアス抵抗
 TM タイミング発生回路
 SEL セクタ回路
 VDD 高圧系電源

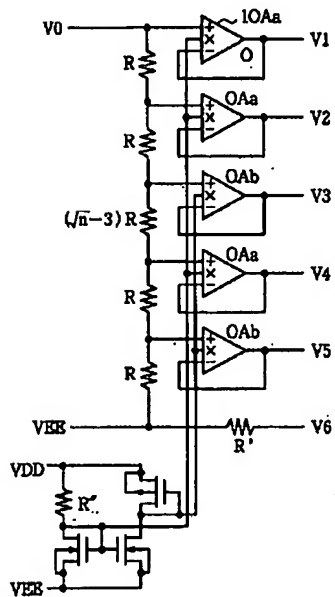
【図2】



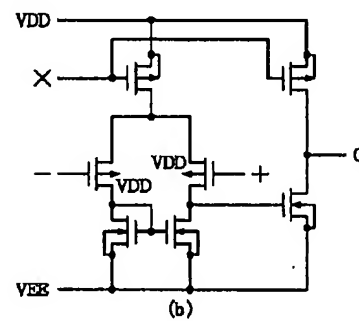
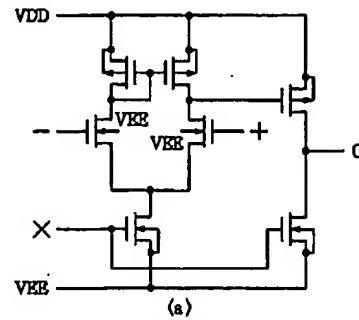
【図1】



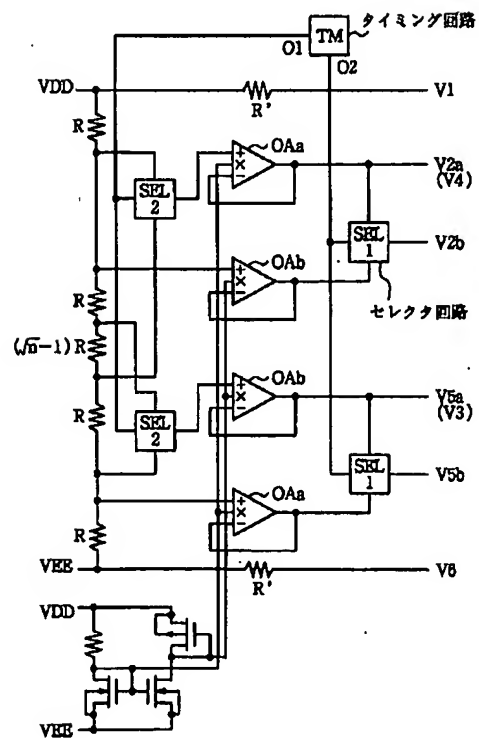
【図6】



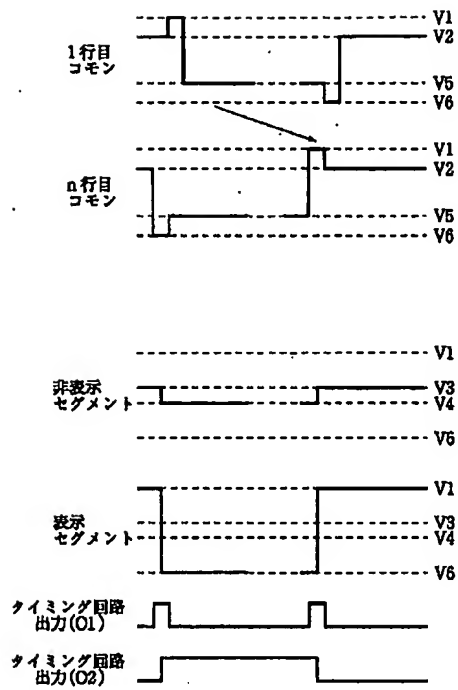
【図3】



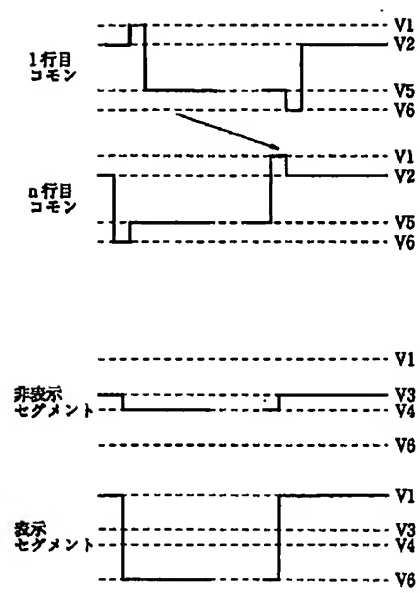
【図4】



【図5】



【図7】



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CLAIMS

[Claim(s)]

[Claim 1] The resistance partial pressure circuit which has two or more resistance which pressures partially between the 1st power-source potential and the 2nd power-source potential, The 1st operational amplifier circuit which receives the output voltage of this resistance partial pressure circuit in an input, and carries out charge actuation of the liquid crystal display component, The 2nd operational amplifier circuit which receives the output voltage of said resistance partial pressure circuit in an input, and carries out discharge actuation of said liquid crystal display component, The power circuit for liquid crystal actuation characterized by having said 1st operational amplifier circuit, the 1st switching circuit which switches said 2nd operational amplifier circuit to predetermined timing, and the timing circuit which controls this 1st switching circuit.

[Claim 2] The power circuit for liquid crystal actuation according to claim 1 characterized by having the 2nd switching circuit which receives the output voltage of said resistance partial pressure circuit, and switches the supply level electrical potential difference of said resistance partial pressure circuit by said timing circuit.

[Claim 3] The power circuit for liquid crystal actuation according to claim 2 which said the 1st switching circuit and said 2nd switching circuit synchronize by said timing circuit, and operates

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DETAILED DESCRIPTION

[Detailed Description of the Invention]

[0001]

[Field of the Invention] This invention relates to a liquid crystal display, especially the number of time sharing (duty) is related with the suitable power circuit for liquid crystal actuation for a large medium size or a liquid crystal display component large-sized dot-matrix type.

[0002]

[Description of the Prior Art] If drawing 6 is referred to, the conventional liquid crystal actuation circuit will pressure partially the electrical-potential-difference difference of a plus side power source (for example, +10V) and a minus side power source (for example, 0V) for the liquid crystal electrical potential difference in this liquid crystal actuation circuit with internal resistance, and will make what carried out impedance conversion with the voltage follower mold operational amplifier further direct liquid crystal driver voltage.

[0003] Such a conventional liquid crystal actuation circuit is indicated by JP,5-273932,A. In this conventional circuitry, when driving a medium size or a large-sized liquid crystal display component, the actuation capacity of each operational

amplifier is improved and used.

[0004]

[Problem(s) to be Solved by the Invention] However, there is a trouble shown below in conventional circuitry.

[0005] (1) Since the actuation capacity of a power source is so much needed when the number of display devices of liquid crystal becomes large, it will be necessary to improve the actuation capacity of each operational amplifier, and the consumed electric current will become large.

[0006] (2) Although a liquid crystal device generally makes possible what is displayed by impressing an electrical potential difference to a common electrode and segment inter-electrode. If the number of several segment ball display lines increases, since the common electrode is common, When the number of duty is n although each common ** or the load carrying capacity to cut becomes large (since a liquid crystal device is volume load) as it is shown in drawing 6 for example, It is related commonly [eye n watch], the charge and discharge from a liquid crystal load compare commonly from the 1st ($n-1$) to [watch], and a discharge charge is large. When the n -th most common actuation capacity is considered as a result, it is necessary to use an operational amplifier with the capacity of both charge discharge, and there is a problem that power consumption cannot be made small as much as possible.

[0007]

[Means for Solving the Problem] The resistance partial pressure circuit which has two or more resistance to which the power circuit for liquid crystal actuation of this invention pressures partially between the 1st power-source potential and the 2nd power-source potential, The 1st operational amplifier circuit which receives the output voltage of this resistance partial pressure circuit in an input, and carries out charge actuation of the liquid crystal display component, The 2nd operational amplifier circuit which receives the output voltage of said resistance partial pressure circuit in an input, and carries out discharge actuation of said liquid crystal display component, It is the configuration of having said 1st

operational amplifier circuit, the 1st switching circuit which switches said 2nd operational amplifier circuit to predetermined timing, and the timing circuit which controls this 1st switching circuit.

[0008] Moreover, it can also consider as the configuration which has the 2nd switching circuit which receives the output voltage of said resistance partial pressure circuit, and switches the supply level electrical potential difference of said resistance partial pressure circuit by said timing circuit.

[0009] Said the 1st switching circuit and said 2nd switching circuit of the power circuit for liquid crystal actuation of this invention can also consider as the configuration which synchronizes by said timing circuit and operates further again.

[0010]

[Embodiment of the Invention] The gestalt of operation of the 1st of this invention is explained with reference to drawing.

[0011] If drawing 1 is referred to, the power circuit for liquid crystal actuation of the gestalt of this operation will carry out impedance conversion of the resistance R which carries out potential division of between both the power sources of the plus side power source V_0 and the minus side power source V_{EE} , and the electrical potential difference divided, respectively, and will be equipped with the operational amplifier (OAa, OAb) which supplies 7 level of the power source for liquid crystal device actuation (V_1 , V_{2a} , V_{2b} , V_3 , V_4 and V_a , V_{5b}). Furthermore, it has the timing generating circuit TM which controls the selector circuit SEL and selector circuit SEL which switch Amplifier (it is shown in A and B common n-th wave edge) OAa and OAb by change timing as shown in drawing 2 which took into consideration the sense of the charge and discharge to liquid crystal load carrying capacity about electrical-potential-difference V_{2b} and V_{5b} , and output electrical-potential-difference V_{2b} and V_{5b} , respectively. When each reference voltage (V_1 - V_6) and the response by being common make an example duty several n liquid crystal equipment, eye common 1st- (n-1) watch will choose each level of an electrical potential difference V_1 , V_{2a} , V_{5a} , and V_6 , and the common n-th will choose electrical-potential-difference V_1 , V_{2b} , V_{5b} , and V_6 level.

[0012] Next, if setting out of the resistance R on a circuit is explained, first, Resistance R only pressures partially between [V0-VEE] power sources fundamentally, and in order to make a current value small, it will choose the thing of a big constant. Moreover, resistance R' chooses a constant almost equal to the impedance of an operational amplifier (OAa or OAb), and equalizes the whole balance.

[0013] R " of resistance is the resistance which determines the current value passed in the low current circuit in each operational amplifier, and in order to make the whole consumed electric current small, it chooses the big thing about several 100Komega.

[0014] Moreover, by setting up in about dozens of microseconds in consideration of a segment and the response time of a common output about the pulse width t_s and t_h shown in drawing 2 , time amount is enough taken so that there may be no effect on the display by gap of timing.

[0015] Next, the gestalt of operation of the 2nd of this invention is explained with reference to drawing 4 and drawing 5 .

[0016] If drawing 4 is referred to, except having further the selector circuit SEL2 which switches the output voltage of two pieces among the output voltage of resistance R' which adjusts the 1st power-source potential V0 to the level electrical potential difference V1, and a resistance partial pressure circuit, the gestalt of this operation is the same configuration as the gestalt of the 1st operation, gives the same reference mark to the same component, and is illustrated.

[0017] Timing circuit TM of the gestalt of this operation synchronizes and outputs the output 01 which drives a selector circuit SEL1 as shown in drawing 5 $R > 5$, and the output 02 which drives a selector circuit SEL2, and the liquid crystal display driver voltage (V1-V6) of the gestalt of this operation outputs the wave shown in drawing 5 by switch of selector circuits SEL1 and SEL2, respectively.

[0018] The gestalt of operation of the 2nd of this invention is circuitry in the case of adjusting an electrical potential difference V0, and when supplying from the

interior or the exterior by using plus side power-source potential V0 as the high voltage system power source VDD, it is not necessary to carry out impedance conversion of it using the operational amplifier circuit OAa. Therefore, the operational amplifier circuit OAa of an output level V1 becomes unnecessary. Moreover, OAa of the operational amplifier OAa which inputs output-level V2a, and the operational amplifier which inputs level V4 is that it is possible to use in common since it is not chosen simultaneously, and the operational amplifier OAb which inputs an output level V5, and the operational amplifier OAb which inputs an output level can also be shared within the same time amount. Therefore, it becomes possible to delete three pieces and to acquire the same effectiveness from the gestalt of the 1st operation of an operational amplifier circuit, and low-power-ization can be achieved further.

[0019]

[Effect of the Invention] As explained above, according to this invention, there is effectiveness which makes small power consumption of the semiconductor device for liquid crystal actuation which can prevent a wave-like distortion depended insufficient [the actuation capacity of a power source] and aggravation of image quality, and has a power circuit also in a display with a large capacity of liquid crystal material and the liquid crystal display component of a large-sized panel.

[Translation done.]

* NOTICES *

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DESCRIPTION OF DRAWINGS

[Brief Description of the Drawings]

[Drawing 1] It is the circuit diagram of the gestalt of operation of the 1st of the power circuit for liquid crystal actuation of this invention.

[Drawing 2] It is the output timing wave of a timing circuit shown in drawing 1 .

[Drawing 3] It is the circuit diagram of the operational amplifier circuit shown in drawing 1 .

[Drawing 4] It is the circuit diagram of the gestalt of operation of the 2nd of this invention.

[Drawing 5] It is the output wave of a timing circuit shown in drawing 4 .

[Drawing 6] It is the circuit diagram of the conventional power circuit for liquid crystal actuation.

[Drawing 7] It is common and the timing chart which shows a segment wave at the time of the electrical potential differences V1-V6 of drawing 6 , and liquid crystal actuation.

[Description of Notations]

V1-V6 Liquid crystal driver voltage

V0 Plus power source

VEE Minus power source

OAA, b Operational amplifier

R Bleeder resistance

R' Auxiliary resistance

R" Bias resistance

TM Timing generating circuit

SEL Selector circuit

VDD High voltage system power source

[Translation done.]

* NOTICES *

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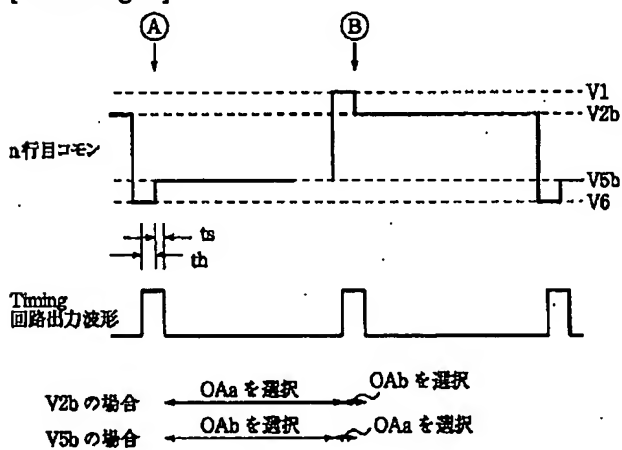
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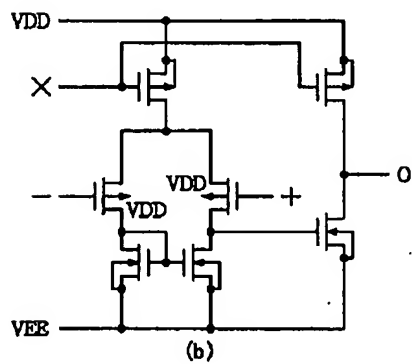
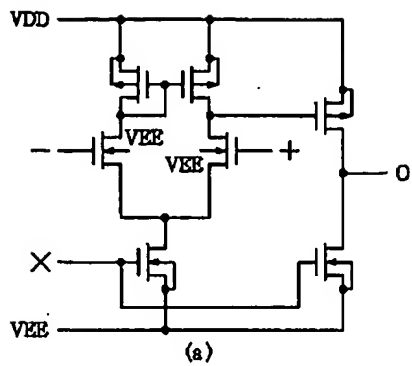
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DRAWINGS

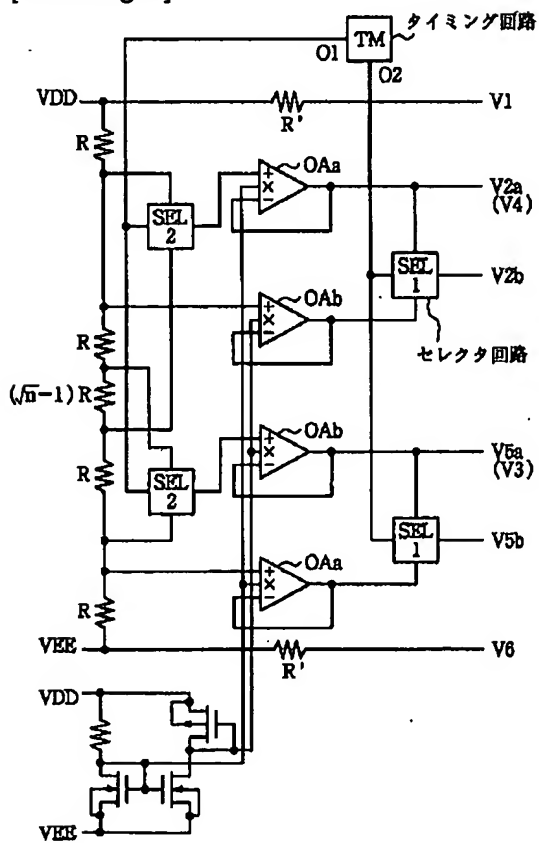
[Drawing 2]



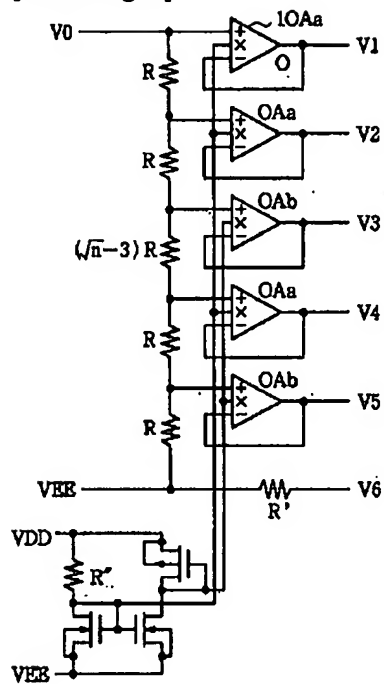
[Drawing 1]



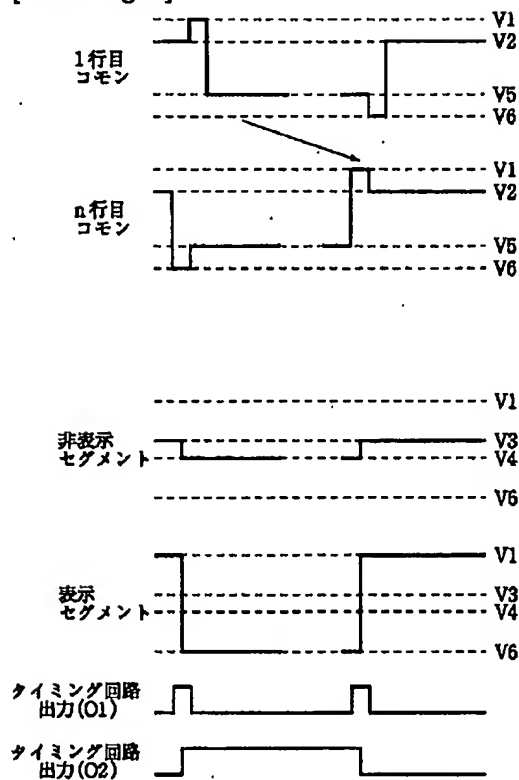
[Drawing 4]



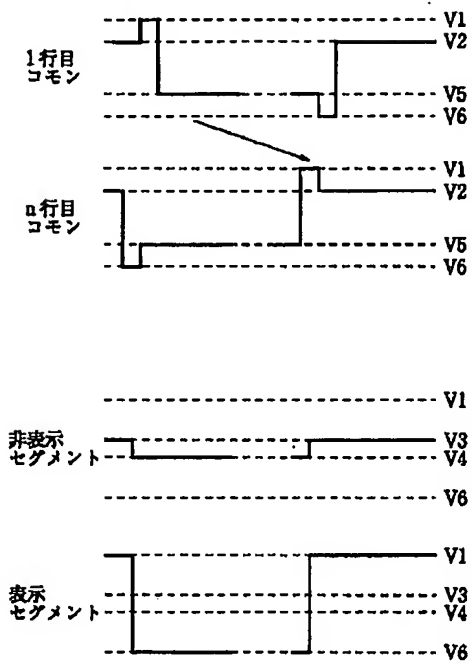
[Drawing 6]



[Drawing 5]



[Drawing 7]



[Translation done.]